



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Craig T. Salling et al

Art Unit: 2815

Serial No. 10/051,639

Examiner: Landau

Filed: 01/18/02

Docket: TI-32536

For: ESD IMPROVEMENT BY A VERTICAL BIPOLAR TRANSISTOR WITH LOW  
BREAKDOWN VOLTAGE AND HIGH BETA

November 25, 2003

Commissioner For Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

AMENDMENT - 37 CFR 1.116

Sir:

Responsive to the Final Rejection of September 25, 2003, **please amend claims 1, 3 and 6** as indicated in the attached pages, which include a marked-up copy and a clean copy of replacement pages.

REMARKS

Favorable reconsideration and allowance of the application are respectfully requested in view of the above amendments and the following comments.

The amendments to claims 1, 3, and 6 are believed to comply fully with all the objections and the rejections presented in the Final Rejection.

The Examiner is encouraged to propose an **Examiner's Amendment**, if additional changes are necessary.

Respectfully submitted,

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TECHNOLOGY CENTER 2800

DEC - 9 2003

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